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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of:

NAKAJYO, Shinsuke et al.

Serial Number: **09/686,958**

Filed: **October 12, 2000**

Examiner: **Michael K. Luhrs**

Group Art Unit: **2824**

P.T.O. Confirmation No.: 5708

For. **MANUFACTURE OF WAFER LEVEL SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE**

POWER TO FILE CORRECTED FORMAL DRAWINGS

Commissioner for Patents
Washington, D.C. 20031

Date: *November 26, 2002*

Sir:

The undersigned, attorney of record in the above-identified application, hereby instructs Frankie Locklear and Element Patent Drafting, to correct the drawings, as required and approved in such above-identified application and to file such corrected formal drawings in the United States Patent and Trademark Office in such application.

In the event that this paper and such formal drawings are not timely filed, Applicants respectfully petition for an appropriate extension of time. The fees for such an extension or any other fee which may be due with respect to this paper, may be charged to Deposit Account No. 01-2340.

Respectfully submitted,

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